

PRIORITY CONTROL METHOD

FIELD OF THE INVENTION

5 The present invention relates to priority control method, and, more particularly, a priority control method, wherein, in a packet exchange unit having feedback convergence control, in order to prevent a packet not undergoing convergence control from a subsequent exit path from being deposited because the output queue is single and its head portion undergoes convergence control from the exit path so that the output queue is not output, a packet undergoing (or which may undergo) convergence control from the exit path and a packet not undergoing the convergence control are exchanged with each other in a range including the output queue and then, the packet not undergoing the convergence control is output with priority so as to improve availability of the output path, thereby reducing total capacity necessary for storing the packet in the output queue and preventing the priority of the order of a packet undergoing convergence detection by limiting an order exchanging range from being reduced too much.

BACKGROUND OF THE INVENTION

10 In a conventional priority control using a single output queue, because its output order is fixed, if a convergence detecting signal is input into a head packet, such a phenomenon that a subsequent packet cannot be output although it can be output because it does not receive a convergence detecting signal often occurs.

15 Because of this phenomenon, a sufficient capacity for storing successively input packets is necessary.

20 According to conventional technologies disclosed in for example, Japanese Patent Application Laid-Open No. 7-135512, Japanese Patent

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Application Laid-Open No. 10-65733, Japanese Patent Application Laid-Open No. 10-243016 and the like, an increase of the capacity is suppressed by discarding an input packet early (Early Packet Discard method) or discarding a packet in a single input queue at random (Random Early Discard method).

Further, although a solution method in which plural queues are possessed for each output path has been proposed, separate controls for each path are necessary, so that a complicated control is required.

SUMMARY OF THE INVENTION

Accordingly, the present invention has been achieved in views of the conventional problems and therefore, an object of the invention is to provide a novel priority control method capable of solving the aforementioned problem existing in the conventional technology.

To achieve the above object, according to an aspect of the present invention, there is provided a priority control method using a single output queue wherein an output priority of a packet not undergoing convergence is improved by exchanging the order of a packet undergoing convergence or a packet which may undergo convergence with the order of a packet not undergoing convergence.

According to a preferred embodiment of the present invention, by setting an operation range for exchanging the order of the packet in a predetermined range, a priority of a packet whose priority is reduced because the packet undergoes convergence is prevented from being reduced too much.

According to another aspect of the present invention, there is provided a priority control method using a packet exchange unit having a single output queue wherein an output portion of the packet exchange unit includes a single output queue having first and second output paths

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for storing a packet input from an input path in order and outputting the stored packet in order; a queue control means for unless the first and second convergence notice signals are input, outputting a packet input to the queue to the first and second output paths in the input order; and a back/forth packet comparison and exchange means for control so as to, if a first or second convergence notice signal is input, output a queue exchange instruction to individual packets scheduled to be output to the first or second output path, existing in a predetermined range of the output queue, thereby exchanging the order of output of packets not undergoing convergence control and outputting the packet not undergoing convergence control with priority, thereby improving availability of the output path.

According to a preferred embodiments of the present invention, the predetermined range including the output signal queue is set to a range controllable by the back/forth packet comparison and exchange means and not too large.

According to a preferred embodiment of the present invention, the exchange of output order of a packet not undergoing convergence control by the queue exchange instruction comprises: a first step of detecting an exit path in which a convergence notice signal is input; a second step of searching a portion including arranged packets of an exit path in which the convergence notice signal is not input, in the predetermined range and just after a packet to be sent to the exit path; a third step of exchanging both the packets in the portion; and a fourth step of, if a convergence notice signal is not input in a head packet after that, reading the packet normally and outputting the packet to the first or second output path.

According to a preferred embodiment of the present invention, the order exchange of the packet is carried out within a packet sending

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time interval.

BRIEF DESCRIPTION OF THE DRAWINGS

Fig. 1 is a block diagram of an output portion in a packet exchange apparatus in which an output queue for use in the priority control of the present invention is single;

Fig. 2 is a flowchart showing an example of operation for exchanging queue of the present invention;

Fig. 3 is a block configuration diagram showing an example of connection of a first packet exchange apparatus having an input path and three output paths;

Fig. 4 is a timing chart showing input/output condition of the packet at each packet time in output queue; and

Fig. 5 is a diagram showing an average output band width in a sequential operation if it is assumed that the sizes of all packets are equal.

DESCRIPTION OF THE PREFERRED EMBODIMENTS

Hereinafter, the preferred embodiments of the present invention will be described in detail with reference to the accompanying drawings.

Fig. 1 is a block diagram of an output portion in a packet exchange apparatus in which an output queue for use in the priority control of the present invention is single.

[Configuration of the embodiment]

Referring to Fig. 1, a packet input through an input path 10 is stored successively into a single output queue 2.

If a first convergence notice signal 14 or a second convergence notice signal 15 is not input into a back/forth packet comparison and exchange means 4, an input packet is output to a first output path 12

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or a second output path 13 in the order of input by an I/O control signal 5 from a queue control portion 3. In order to determine an output path at this time, a well known method using the packet header is employed.

If the first convergence notice signal 14 or the second convergence notice signal 15 is input to the back/forth comparison and exchange means 4, a queue exchange instruction 7 is output to individual packets to be output to the first output path 12 or the second output path 13 in a predetermined range 20 of the output queue 2.

The predetermined range 20 is set up to a range which can be controlled by the back/forth packet comparison and exchange means 4 and is not too wide. If the predetermined range 20 is set wide, the priority of a packet in an exit path in which the convergence notice signal is input may sharply drop when the convergence notice signal is released.

The queue exchange instruction 7 exchanges individual packets following a flow chart shown in Fig. 2.

First, an exit path in which the convergence notice signal is input is detected (condition branch step S1).

Next, a portion of the exit path where a packet not containing a convergence notice signal is arranged just after a packet containing the convergence notice is searched in the predetermined range 20 (condition branch step S2).

An order of both the packets in this portion is exchanged (step S3).

After that, if the head packet contains no convergence notice signal, normal packet reading is carried out and its result is output to the first output path 12 or second output path 13 (step S4).

The order exchange is carried out within a packet sending time interval.

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According to this embodiment, there are two output paths and convergence notice signals. However, they may be more than two and the output path itself may be physical individual path or logical common path.

5 Setting and change of the parameter in the queue control portion 3 are carried out by a control apparatus (not shown) through the control signal 11.

Although in the above description, the back/forth packet comparison and exchange means is composed in software, it may be composed in hardware using a comparison means, memory and the like.

[Operation of the Embodiment]

Next, an operation of one embodiment of the present invention will be described with reference to drawings.

Fig. 3 is a block structure diagram showing an example of a connection structure of a first packet exchange unit 1 having an input path and three output paths.

Referring to Fig. 3, a packet output from an information sending unit 31 is exchanged by the first packet exchange unit 1 and finally, input to a first information receiving unit 33, second information receiving unit 34 and third information receiving unit 35.

A second packet exchange unit 32 having two input paths and two output paths is provided on paths leading to the first information receiving unit 33 and second information receiving unit 34 and executes the same operation as the first packet exchange unit 1. In this example, in the first output path 12 and first convergence notice signal 14, output paths and convergence notice signal to two receiving units which are the first information receiving unit 33 and the second information receiving unit 34 are logically multiplexed.

Fig. 4 is a diagram (timing chart) showing I/O condition of a

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packet at each packet time in output queue 2.

Referring to Fig. 4, a predetermined interval 20 is for example, six packets. It is assumed that the same quantity of the packet is sent from the information sending unit 31 to the first to third information receiving units 33 to 35 and that a convergence notice signal from the first information receiving unit 33 is input into the first packet exchange unit 1 in an interval from time t_0 to t_4 . Here, A0-A6, B0-B6 and C0-C6 indicate packets to be output to the first information receiving unit 33, second information receiving unit 34 and third information receiving unit 35, respectively.

Because the convergence notice signal is input to the head packets A0-A1 at time t_0 , the order of the packet is exchanged in a predetermined range 20 according to the flow chart of Fig. 2. By exchanging the order, the head packet at time t_1 is B0 and output without undergoing the convergence control. Likewise, the exchange of the order and output are repeated from time t_1 to t_4 .

Because the convergence notice signal is released after time t_5 , the order of the packet is not exchanged and the packets are output successively from the head.

Because during the above-described operation, the availability of the output path is not reduced, total capacity necessary for storing the packet in the single output queue 2 is not changed and such an event that the packet is not input but discarded never occurs.

Further, such an event that after the convergence notice signal is released, the order priority in queue A0-A6 drops too much never occurs.

Fig. 5 shows an average output bandwidth in a sequential operation if it is assumed that the sizes of all packets are equal. In Fig. 5, the input band width is totally 3 (1 each for A, B, C).

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The present invention has the above-described structure and operation. According to the present invention, following effects are secured.

1. It is possible to suppress such a phenomenon that a rear portion
5 not undergoing convergence control of a single queue cannot be output because a head packet undergoes the convergence control.

2. Because a possibility that the output path may be clogged by a convergence detecting signal is reduced, total capacity necessary for a single output queue can be reduced.

10 3. By setting a predetermined range in which the packet order is exchanged appropriately, it is possible to prevent the priority of a packet in a output path which receives a convergence detecting signal and after a while, is released from the convergence detecting signal from being lowered excessively.

15 Although the invention has been described with respect to specific embodiment for complete and clear disclosure, the appended claims are not to be thus limited but are to be construed as embodying all modification and alternative constructions that may be occurred to one skilled in
20 the art which fairly fall within the basic teaching herein set forth.